

## REGISTRATION FORM

### Faculty Development Program On

### ***ELECTRONIC DESIGN AUTOMATION***

18.12.17 to 22.12.17

Name of the Faculty :  
Category : Faculty member/Research Scholar  
Designation :  
Institution :  
Address for Communication :  
Contact Number :  
Email ID :  
**Payment Details**  
Amount :  
DD No & Date :  
Bank :

Signature of the Faculty Member

### SPONSORSHIP CERTIFICATE

Certified that Dr./Prof./Mr./Ms. \_\_\_\_\_

\_\_\_\_\_ is a bonafide faculty member of our Institution. He / She is identified to attend the workshop "ELECTRONIC DESIGN AUTOMATION". He/ She is sponsored for the same.

Date:  
Place:

Signature and Seal of the Principal/ Director

## ORGANIZING COMMITTEE

Patron : Mar George Njaralakatt  
Archbishop of Tellicherry

Chairman : Rev. Fr. Dr. Thomas Melvettath  
Chairman  
Vimal Jyothi Engineering College  
Chemperi

Secretary: Prof.Roshini T V  
Head of Department ECE  
Vimal Jyothi Engineering College  
Chemperi

Organizing Committee:

Dr Benny Joseph, Principal Vimal Jyothi Engineering College  
Rev. Fr. George Asarikunnel  
Rev. Fr. Jinu Vadakkemulanjanal  
Prof.Jerrin Yomas  
Prof. Manoj K C  
Prof.Vinod J Thomas  
Prof.Binil Kumar  
Prof.Bindu Sebastian  
Prof. Jayesh George  
Prof. Adarsh K S

### ADDRESS FOR COMMUNICATION

Prof. ANOOP B.K

Co-Ordinator,

FDP on ELECTRONIC DESIGN AUTOMATION

Dept.of Electronics and Communication Engineering,  
Vimal Jyothi Engineering College Jyothi Nagar,  
Chemperi, Kannur – 670632

Mobile No: +919447012013

Email ID: anoop013@vjec.ac.in

## APJ Abdul Kalam Technological University



Sponsored Faculty Development Program on

***ELECTRONIC DESIGN AUTOMATION***

**18.12.17 to 22.12.17**

### COURSE COORDINATORS

Prof. Dr . I .Selvamani

Prof. Anoop B K

### ORGANIZING SECRETARY

Prof. Roshini T V

ORGANIZED BY

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



**VIMAL JYOTHI ENGINEERING  
COLLEGE CHEMPERI KANNUR**

## **ABOUT THE COLLEGE**

Vimal Jyothi Engineering College, Chemperi is one of the fast growing, ISO 9001:2008 certified, NBA accredited, self financing, minority institution located at the hill valley of Malabar, the northern part of Kerala. The college is an educational Project of Archdiocese of Thalassery and is managed by the Meshar Diocesan Educational Trust, Archbishop's House, Thalassery, 670 101. Mar George Njaralakatt, Archbishop of Thalassery is the Patron and Very Rev. Fr. Dr. Thomas Melvettath is the Chairman of the trust. Dr. Benny Joseph B.E.(Civil), M. Tech(Environmental Engg.), Ph.D is the Principal of the College. Rev Fr George Asarikunnel is the Bursar and Rev.Fr.Jinu Vadakkemulanjanal, is the Administrator of Vimal Jyothi. Vimal Jyothi was established in the year 2002. The college is affiliated to Kannur university and APJ Abdul Kalam Technological University . All India Council for Technical Education[AICTE] has updated its approval status for the current academic year for the following B.Tech Degree Programs Mechanical Engineering,Electrical and Electronics Engineering,Electronics and Communication Engineering,Computer Science and Engineering, Applied Electronics and Instrumentation Engineering and Civil Engineering

## **ABOUT THE DEPARTMENT**

The Electronics and Communication Engineering Dept was established in 2002 with an intake of 60 students in the B.Tech program. Now we have 2 batches (120 intake) of ECE B Tech Program and M Tech Communication Engineering and Signal Processing of 24 students. The Dept has well qualified and experienced faculty.

The staff members are deputed to participate in workshops, conferences and refresher courses to keep in pace with the recent development in the field of Electronics & Communication Engineering. The Department has good infrastructural facilities and is equipped with full fledged laboratories

## **ABOUT ELECTRONIC DESIGN AUTOMATION**

Electronic design automation (EDA) is a category of software tools for designing electronic systems such as printed circuit boards and integrated circuits. The tools work together in a design flow that chip designers use to design and analyze entire semiconductor chips

## **TARGET AUDIENCE**

The faculty development program is aimed at the multi-faceted audience from Academics and Researchers from Engineering Colleges and Universities. No TA and DA will be provided for the participants. The Number of seats is limited to 30.

## **ACCOMMODATION**

Accommodation will be provided on prior request

## **COURSE CONTENT**

- Electronic System Design
- Circuit Analysis
- OrCAD
- Pspice

## **REGISTRATION FEE**

No course fee is charged from participants sponsored by Government /Govt. Aided Engineering Colleges/ self financing/ TEQIP funded educational institutions

## **RESOURCE PERSONS**

Eminent Faculty members from various top level Academic Institutes. Including IIT & NIT

## **IMPORTANT DATES**

Last Date for Registration : 16.12.2017