

Government Engineering College, Sreekrishnapuram
Three Day FDP on
Power Electronics and Embedded System
Theory and Practice
(3rd December to 5th December 2018)
Registration Form

Name :

Gender :

D.O.B & Age :

Designation :

Institution :

Qualification :

Experience in Years :

Address for Communication. :

(With Telephone No & E-mail ID)

Preferred Cuisine : Veg./Non-Veg.

Place:

Date: Signature of the Applicant

Sponsorship Certificate

Mr./Ms. _____ is an employee of our Institution / Organization and is hereby sponsored. He/She will be permitted to attend the short-term training programme for the entire duration, if selected.

Name and Signature of the
Head of the Institution

College Seal

Place:

Date:

Important Dates

Last Date for Application : 26.11.2018

Intimation by E-mail : 27.11.2018

Address for Communication

Dr. A R Jayan (HOD)

Associate Professor and HOD, Dept. of ECE,

E-mail: arjayan71@gmail.com

Mobile: +91 - 8129418002

Hariprasad B (Coordinator)

Asst. Professor, Dept. of ECE,

E-mail: hpmalabar@gmail.com

Mobile: +91 - 9895193772

Shanooja M A (Co-Coordinator)

Asst. Professor (Adhoc), Dept. of ECE,

E-mail: shanu.noushi@gmail.com

Mobile: +91 - 7025053441



Government Engineering College
Sreekrishnapuram, Palakkad,
Mannampatta P.O Pin:678633

Faculty Development Programme

On

Power Electronics and Embedded System – Theory and Practice

3rd to 5th December 2018 (3 Days)

Organized by

Dept. of Electronics and Communication
Engineering



Sponsored By

TEQIP Phase – II

Govt. Engineering College,
Sreekrishnapuram, Palakkad

*“Excellence through the Wings of Science and
Technology”*



ABOUT THE COLLEGE

Govt. Engineering College, Sreekrishnapuram Palakkad, an institution of academic excellence was established in 1999 by the Govt. of Kerala and is under the Directorate of Technical Education, Kerala. The College is approved by AICTE and affiliated to APJ Abdul Kalam Technological University. The college functions under the guidance of Dr.P C Reghuraj. The college presently offers five under-graduate courses and one post graduate course. Institution offers PHD under KTU since 2016. The institution is equipped with state of art infrastructure, well equipped laboratories, and a library with ample supply of books, periodicals and subscription to international journals.

ABOUT THE DEPARTMENT

The department has over the years developed into a full-fledged centre of excellence for exploring the fast developing areas in the field of electronics and communication to the maximum extent possible. The department is well supported by the state of art facility and a team of well qualified and motivated faculty.

ABOUT THE PROGRAMME

The FDP is planned to provide an open platform to disseminate and to encourage discussions on topics related to Microcontroller based system design and to

give an exposure to power electronic converters with experts from various institutes. The course is also envisaged to strengthen the practical knowledge on PIC and 8051 based system design. The programme will also help the academicians to learn the working of power electronic converters through demonstration which indeed an essential part of the KTU B-Tech curriculum.

WORKSHOP AGENDA

Day 1:

- Power electronic converters
- Demonstration of converter laboratory kits

Day 2:

- PIC Microcontroller
- Hands on session on PIC based system design

Day 3:

- 8051 Microcontroller based system design
- Practical session on 8051 system simulation

RESOURCE PERSONS

Dr. Navas K A, Professor and HOD, ECE Dept., GEC Kannur

Peeyush K P, Assistant Professor, ECE Dept., Amrita Vishwa Vidyapeetham, Coimbatore

Mohammed Salih, Associate Professor, ECE Dept., GEC Thrissur

WHO CAN APPLY

To encourage interdisciplinary research, faculty members from AICTE approved engineering colleges in Kerala and belong to branches related to the above areas are eligible to apply

HOW TO APPLY

The applicants should send their applications, duly sponsored, in the specified format to the course

Coordinator/Co-coordinator on or before 23/11/2018 by e-mail. Please mention on the subject line “**FDP on Power Electronics and Embedded System**”. Applications received after the due will not be considered

REGISTRATION FEE & ACCOMMODATION

No Registration fee will be charged from participants of non-TEQIP institutions. A nominal registration fee of Rs.500/- will be charged for participants from TEQIP funded institutions. Payments can be done at the time of registration by cash. Accommodation and travelling expenses are to be borne by the participants. The participants will not be paid any TA/DA. Course materials, working lunch and refreshments will be provided.

HOW TO REACH US

GEC Sreekrishnapuram is located at Mannampatta in Palakkad district of Kerala. It is about 37 km from Palakkad town, 25 km from Ottapalam and 40 km from Shornur Railway station.

Route Map: <https://goo.gl/maps/znhswbjxCa72>

Department of Electronics & Communication Engg.

Vision: To mould the youth as promising excellent engineers in Electronics and Communication

Mission: To provide a strong base in the theoretical and practical aspects of Electronics & Communication with in-depth knowledge in Mathematics and Science
To build up effective communication skills and develop ability to work together with professional and ethical responsibility.