

## ABOUT THE COLLEGE

Sahrdaya College of Engineering & Technology is one of the most prestigious and preferred institutions of Kerala. The environment in Sahrdaya paves way for an ideal learning ambience providing exposure to different cultures and perspectives for fruitful and meaningful interactions. The inspired mindset of every Sahrdayan helps to provide quality education and inculcate human values in students to embrace new challenges and seize new opportunities. With the completion of 18 years of excellence, the institution has put tremendous efforts to provide the best facilities for the attainment of quality Technical Education. Sahrdaya College has been constantly strengthened in size and infrastructure.

## ABOUT THE DEPARTMENT

The Department of ECE has excellent infrastructural facilities. All the laboratories in the Department are equipped to satisfy the norms laid by AICTE and the APJ Abdul Kalam Technological University. The main objective of the department right from its inception has been to provide high quality training in various fields of Electronics & Communication Engineering. Along with UG programme, the Department also offers M Tech in Embedded Systems and PhD in Signal Processing.



## TARGET GROUPS

Faculty members from KTU Affiliated Colleges can participate in this course. All participants have to get them sponsored from the principal or head of the department and complete the registration process as early as possible.

## Registration process

The number of participants are limited to 30 and participants will be selected on first come first serve basis. Participants must take a printout of the registration form bundled with this brochure and send it along with a cheque of Rs. 1000 (returnable/refundable on participation) to sahrdaya college of engineering & technology, payable at kodakara.

## Objectives

- To provide exposure to fundamentals of signal processing and its applications
- To focus on recent developments and research issues in the field of digital signal processing.
- To make participants understand, learn and use various analysis methods(tools) for signal processing applications

## Course Contents

- Fundamentals of DSP & Filter Design
- Wavelet analysis and its applications in Speech Enhancement
- Audio and Image Compression
- Poly-phase channelizer receivers
- Industrial Design & Applications
- Multi-rate signal processing
- Biomedical signal processing
- Hands on sessions using MATLAB

## Resource Persons



Dr. P. S. Sathidevi, Dean, NIT Calicut



Dr. M. V. Rajesh Associate Professor  
Govt. Model Engineering College, Cochin  
Hon. Secretary, IETE, Chairman CSI, Kochi



Dr. Elizabeth Elias, Rtd. Professor, NIT Calicut &  
Director, SCET



Dr. Varun P. Gopi, Asst. Professor, NIT Trichy

Apart from academicians, sessions will be handled by Scientists from government agencies as well



## KTU Sponsored FDP on DSP & it's Applications

### REGISTRATION FORM

Name :

Department :

Designation :

Organization :

Address :

Phone :

Mobile :

E-mail :

Signature:

Recommendation of Principal  
or HOD of sponsoring institute

Signature with Seal



Scan the QR code given above or logon to  
the link given below to complete registration

<https://tinyurl.com/dspfdp>

## ORGANIZING COMMITTEE

CHIEF PATRON

Rt. Rev. Bishop Dr. Pauly Kannookadan,  
Chairman SCET

PATRONS

Rev. Fr.George Pareman, Exe.Director, SCET  
Rev. Fr.Jinoj Kolenchery, Finance Officer,SCET

CHAIRMAN

Dr. Nixon Kuruvila, Principal,SCET

ADVISORY COMMITTEE

Dr. Elizabeth Elias, Director, SCET  
Dr.Sudha George Valavi, Joint.Director,SCET  
Prof.K.T Joseph, Advisor,SCET

## ADDRESS FOR COMMUNICATION

CONVENOR

Dr. VISHNU RAJAN  
Associate Professor, Dept. of ECE  
Contact Numbers: 8113997771  
E-mail: hodece@sahrdaya.ac.in

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Ms.Binet Rose Devassy  
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Sponsored 3 day FDP  
on

# DSP and its Applications

25th to 27th July 2019

Organized by



Department of Electronics &  
Communication



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