

Faculty Development Programme

on

“The Art of Teaching”

1st February & 3rd February, 2020

REGISTRATION FORM

Name :
Designation :
Department :
Address :
Veg/Non Veg :
Phone :
Email :
Date :
Signature :

SPONSORSHIP

This is to certify that ----- is an employee of our Institution and is hereby sponsored for the two days Faculty Development Programme on “The Art of Teaching” at Vedavyasa Institute of Technology, Malappuram on 1st February and 3rd February, 2020. He/She will be permitted to attend the course for the entire duration if selected.

Signature with seal
Head of the Institution

Place:
Date:

ORGANIZING COMMITTEE

PATRONS

Smt.Kala Korath

Chair Person, Vedavyasa Trust

Dr.Arun Korath

Director, Vedavyasa Trust

Dr.S.Sangheethaa

Principal, Vedavyasa Institute of Technology

CONVENORS

Dr.S.Kavitha Murugesan, Prof, HOD CSE

Dr.C.Periasamy, Prof, HOD ECE

Prof.Balakrishna Pillai Prof, HOD EEE

CO-ORDINATORS

Mr.Rahul I R (Assistant Professor, ECE)

Ms.Anjana P (Assistant Professor, CSE)

Mr.Bibin K Joseph (Assistant Professor, EEE)

RESOURCE PERSONS

Dr.Kumaravel S. Prof, EEE, NIT Calicut

Dr.Baby Shari, Prof, Psychology, Calicut University

Dr.Jaget Babu, Prof, Dean, VVIT, Malappuram

CONTACT

Email: hodcse@vedavyasa.org
Phone: 8086578545, 9061212493,
9445010909, 9496172520

TWO DAYS

FACULTY DEVELOPMENT PROGRAMME



“The Art of Teaching”

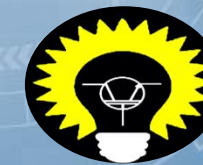
On 1st February & 3rd February, 2020

Organized by

Computer Society of India-VVIT

ACE & AEECE

DEPT OF CSE, ECE, EEE



VEDAVYASA INSTITUTE OF TECHNOLOGY,
KARAD, MALAPPURAM, KERALA. 673632

Tel: 0483 2832157, 9446565077

Website: www.vedavyasa.org

Email: vvit@vedavyasa.org

ABOUT OUR INSTITUTION



The Vedavyasa Institute of Technology is a private self financing engineering college, established in the year 2004 and founded by Vedavyasa Trust. The institute was named after Maharishi Vedavyasa by Gurudev Sri Sri Ravi Shankar. The institute has currently 5 UG programs in Engineering- Civil, CSE, ECE, EEE, ME and 5 PG programs MBA, M.Tech in CSE, Embedded Systems, Power Electronics, Structural Engg. Our institution is blessed with an exquisite infrastructure exhibited through the well equipped and sophisticated laboratories with an intellectual, talented and dedicated team of staff members is striving hard to improve the quality of education with the quality policy to seek, to achieve and never to yield on education, resulting in knowledge enhancement, educational and technological upliftment and professional satisfaction.

ACE & AEECE

Established in 2004 the department of Computer Science and Engineering strives to groom professional computer engineers through Association of Computer Engineers(ACE) and the Depts of ECE and EEE with combined effect had bloomed the technology with various workshops through Association of Electrical Electronics and Communication Engineers (AEECE.)

ABOUT THE CSI-VVIT UNIT

The primary goal of Computer Society of India CSI-VVIT professional body unit is to provide a forum for the dissemination and exchange of latest research findings by academicians and scientists in various related areas of Computer.

ABOUT THE PROGRAMME

The objectives of **“ Art of Teaching“** FDP program are to upgrade the Teaching Faculty & Technical Staff in the domain of Art of Teaching, to increase self-confidence and self-understanding, to enhance communication skills, and improved cognition.

ELIGIBILITY AND SELECTION

Any Faculty from approved (Arts/Science/Mngt/Professional) Institutions can participate in the programme. The total intake is 50 and selection will be based on first come first served basis.

COURSE CONTENTS

- **Motivation to Young Acharyas**
- **Workshop on Outcome Based Education**
- **Art of Instilling Knowledge**

REGISTRATION

Application forms in the respected format duly filled in all respects, along with Sponsorship Certificate from the Head of the Institution should reach the Coordinator on or before 30.1.2020 along with Registration fee of Rs.300 (CSI members –Rs.250). Early information through e-mail for registration is appreciated.

IMPORTANT DATES

Last date for registration : 30-01-2020
Intimation of selection : 31-01-2020

ADDRESS FOR CORRESPONDENCE

Vedavyasa Institute of Technology,
Convener,
FDP on The Art of Teaching,
MALAPPURAM - 673632.
Email: hodcse@vedavyasa.org
Tel: 9061212493, 8086578545

HOW TO REACH VVIT

10 kms From Feroke Railway Station

16 Kms From Calicut Rly Station

08 Kms From Ramanattukara

